## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

For:	apparatus and method for integrated chipset content protection - UTILITY -	
Filed:		
Application No.:		Examiner:
LOUIS A. LIPPINCOTT		Art Group
In re the	e Application of:	

Assistant Commissioner for Patents Washington, D.C. 20231

DUPLICATE

## TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 2 sheets including 2 Figures. Applicant hereby authorizes any additional extension or petition fees under 37 C.F.R. §1.17 or credit for any overpayment to our Deposit Account No. 02-2666. A copy of the Fee Transmittal sheet is enclosed.

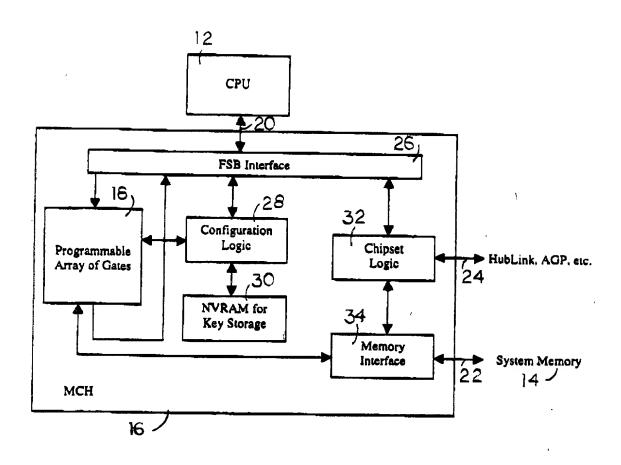
Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Dated: 12/29/00

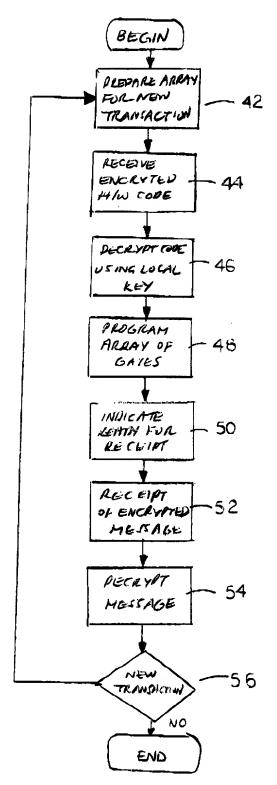
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F16.2